# N-Channel 100 V (D-S) MOSFET

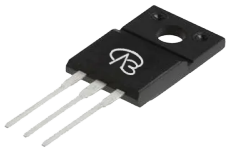
| PRODUCT SUMMARY | | | |
|---|---|---|---|
| $V_{DS}$ (V) | $R_{DS(on)}$ (Ω) MAX. | $I_D$ (A) | $Q_g$ (TYP.) |
| 100 | 0.145at $V_{GS}$ = 10 V | 12 | 60nC |

## FEATURES

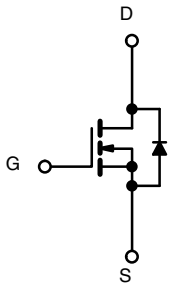
- SGT technology Power MOSFET
- 100 % $R_g$ and UIS tested
- Maximum 150°C junction temperature

RoHS COMPLIANT
HALOGEN FREE

TO-220F



Top View




N-Channel MOSFET

## APPLICATIONS

- Power supplies:
  - Uninterruptible power supplies
  - AC/DC switch-mode power supplies
  - Lighting
- Synchronous rectification
- DC/DC converter
- Motor drive switch
- DC/AC inverter
- Solar micro inverter
- Class D audio amplifier

| ABSOLUTE MAXIMUM RATINGS ($T_C$ = 25 °C, unless otherwise noted) | | | | |
|---|---|---|---|---|
| PARAMETER | | SYMBOL | LIMIT | UNIT |
| Drain-Source Voltage | | $V_{DS}$ | 100 | V |
| Gate-Source Voltage | | $V_{GS}$ | ± 20 | |
| Continuous Drain Current ($T_J$ = 150 °C) | $T_C$ = 25 °C | $I_D$ | 12 | A |
| | $T_C$ = 70 °C | | 10 | |
| Pulsed Drain Current (t = 100 µs) | | $I_{DM}$ | 36 | |
| Avalanche Current | L = 0.5 mH | $I_{AS}$ | 60 | |
| Single Avalanche Energy [a] | | $E_{AS}$ | 790 | mJ |
| Maximum Power Dissipation [a] | $T_C$ = 25 °C | $P_D$ | 300[b] | W |
| | $T_C$ = 100 °C | | 150[b] | |
| Operating Junction and Storage Temperature Range | | $T_J$, $T_{stg}$ | -55 to +150 | °C |

| THERMAL RESISTANCE RATINGS | | | |
|---|---|---|---|
| PARAMETER | SYMBOL | LIMIT | UNIT |
| Junction-to-Ambient (PCB Mount) [c] | $R_{thJA}$ | 40 | °C/W |
| Junction-to-Case (Drain) | $R_{thJC}$ | 0.5 | |

**Notes**

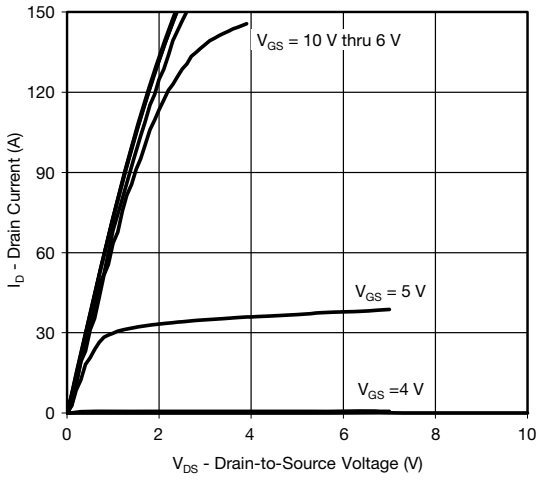
a. Duty cycle ≤ 1 %.
b. See SOA curve for voltage derating.
c. When mounted on 1" square PCB (FR4 material).

| **SPECIFICATIONS** ($T_J$ = 25 °C, unless otherwise noted) | | | | | | |
|---|---|---|---|---|---|---|
| **PARAMETER** | **SYMBOL** | **TEST CONDITIONS** | **MIN.** | **TYP.** | **MAX.** | **UNIT** |
| **Static** | | | | | | |
| Drain-Source Breakdown Voltage | $V_{DS}$ | $V_{GS}$ = 0 V, $I_D$ = 250 µA | 100 | - | - | V |
| Gate Threshold Voltage | $V_{GS(th)}$ | $V_{DS}$ = $V_{GS}$, $I_D$ = 250 µA | 2.5 | - | 4.5 | |
| Gate-Body Leakage | $I_{GSS}$ | $V_{DS}$ = 0 V, $V_{GS}$ = ± 20 V | - | - | ± 250 | nA |
| Zero Gate Voltage Drain Current | $I_{DSS}$ | $V_{DS}$ = 100 V, $V_{GS}$ = 0 V | - | - | 1 | µA |
| | | $V_{DS}$ = 100 V, $V_{GS}$ = 0 V, $T_J$ = 125 °C | - | - | 150 | |
| | | $V_{DS}$ = 100 V, $V_{GS}$ = 0 V, $T_J$ = 150 °C | - | - | 5 | mA |
| On-State Drain Current [a] | $I_{D(on)}$ | $V_{DS}$ ≥ 10 V, $V_{GS}$ = 10 V | 90 | - | - | A |
| Drain-Source On-State Resistance [a] | $R_{DS(on)}$ | $V_{GS}$ = 10 V, $I_D$ = 30 A | - | 0.145 | - | Ω |
| | | $V_{GS}$ = 4.5 V, $I_D$ = 30 A | - | 0.160 | - | |
| Forward Transconductance [a] | $g_{fs}$ | $V_{DS}$ = 15 V, $I_D$ = 30 A | - | 75 | - | S |
| **Dynamic [b]** | | | | | | |
| Input Capacitance | $C_{iss}$ | $V_{GS}$ = 0 V, $V_{DS}$ = 25 V, f = 1 MHz | - | 1500 | - | pF |
| Output Capacitance | $C_{oss}$ | | - | 246 | - | |
| Reverse Transfer Capacitance | $C_{rss}$ | | - | 21 | - | |
| Total Gate Charge [c] | $Q_g$ | $V_{DS}$ = 30 V, $V_{GS}$ = 10 V, $I_D$ = 60 A | - | 60 | 96 | nC |
| Gate-Source Charge [c] | $Q_{gs}$ | | - | 16.7 | - | |
| Gate-Drain Charge [c] | $Q_{gd}$ | | - | 16.9 | - | |
| Gate Resistance | $R_g$ | f = 1 MHz | 1.5 | 3 | 6 | Ω |
| Turn-On Delay Time [c] | $t_{d(on)}$ | $V_{DD}$ = 30V, $R_L$ = 1.66 Ω<br>$I_D$≅ 60 A, $V_{GEN}$ = 10 V, $R_g$ = 1 Ω | - | 18 | 26 | ns |
| Rise Time [c] | $t_r$ | | - | 20 | 40 | |
| Turn-Off Delay Time [c] | $t_{d(off)}$ | | - | 39 | 45 | |
| Fall Time [c] | $t_f$ | | - | 85 | 148 | |
| **Drain-Source Body Diode Ratings and Characteristics [b]** ($T_C$ = 25 °C) | | | | | | |
| Pulsed Current (t = 100 µs) | $I_{SM}$ | | - | - | 36 | A |
| Forward Voltage [a] | $V_{SD}$ | $I_F$ = 20 A, $V_{GS}$ = 0 V | - | 0.8 | 1.2 | V |
| Reverse Recovery Time | $t_{rr}$ | $I_F$ = 20A, di/dt = 100 A/µs | - | 50 | - | ns |
| Peak Reverse Recovery Charge | $I_{RM(REC)}$ | | - | 11 | 20 | A |
| Reverse Recovery Charge | $Q_{rr}$ | | - | 0.9 | 1.8 | µC |

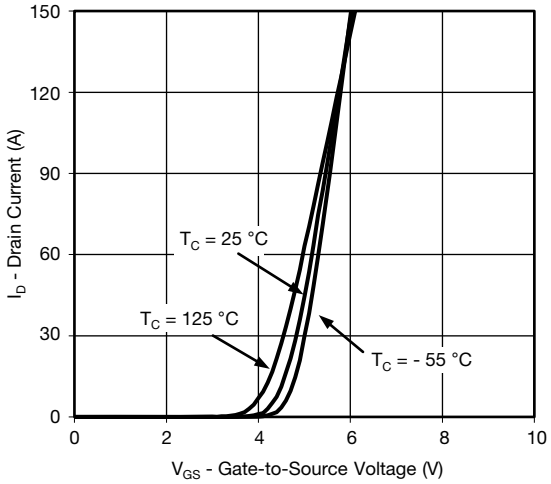
**Notes**

a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
b. Guaranteed by design, not subject to production testing.
c. Independent of operating temperature.

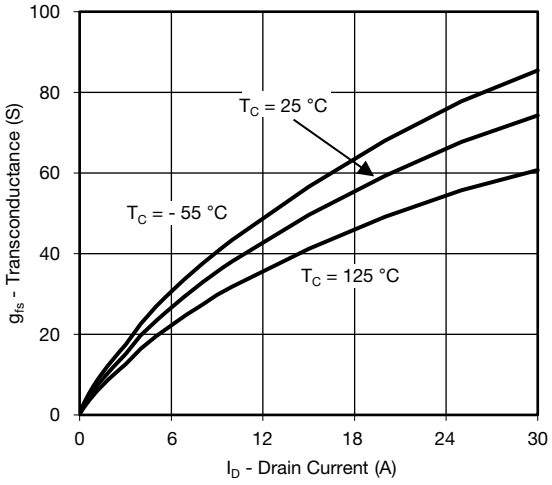
## TYPICAL CHARACTERISTICS ($T_A$ = 25 °C, unless otherwise noted)



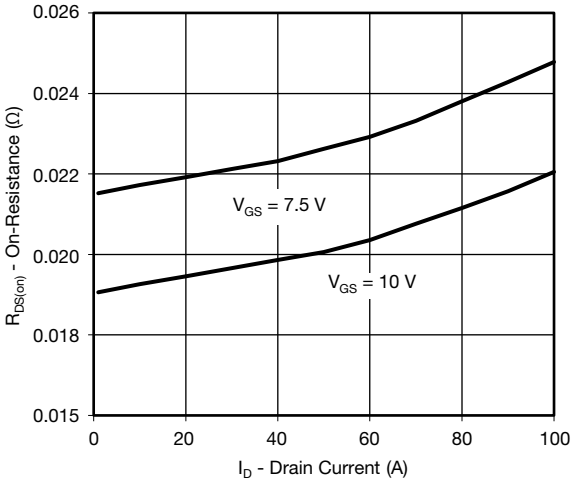

**Output Characteristics**



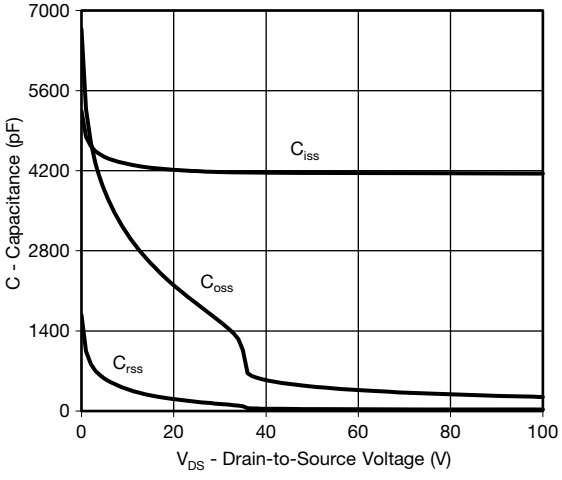

**Transfer Characteristics**



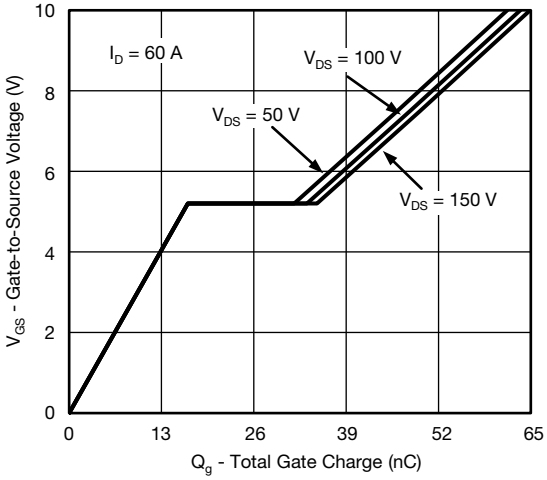

**Transconductance**




**On-Resistance vs. Drain Current**

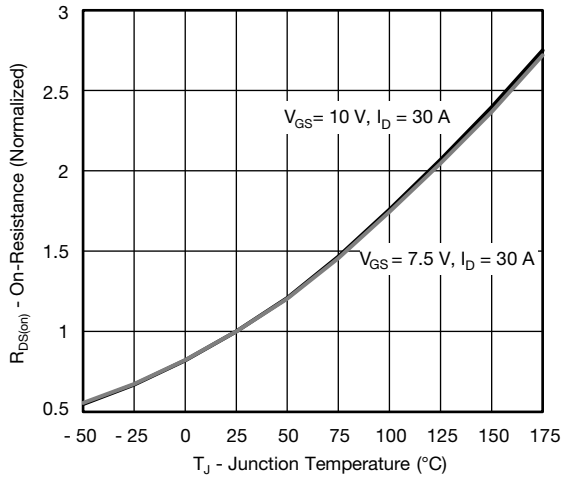



**Capacitance**

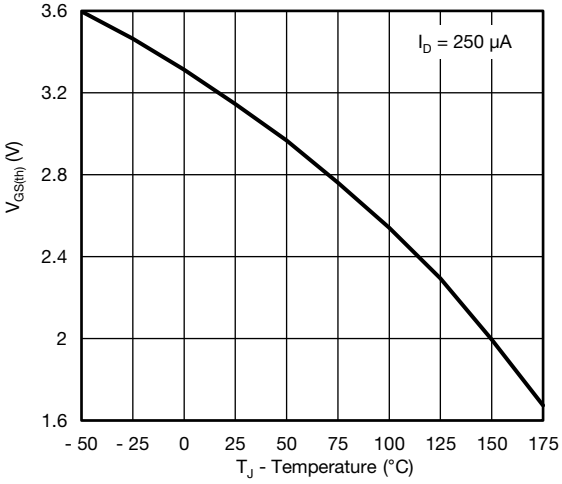



**Gate Charge**

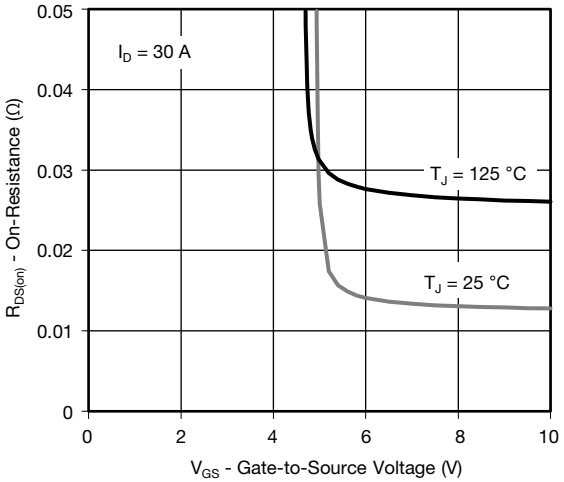
## TYPICAL CHARACTERISTICS ($T_A$ = 25 °C, unless otherwise noted)



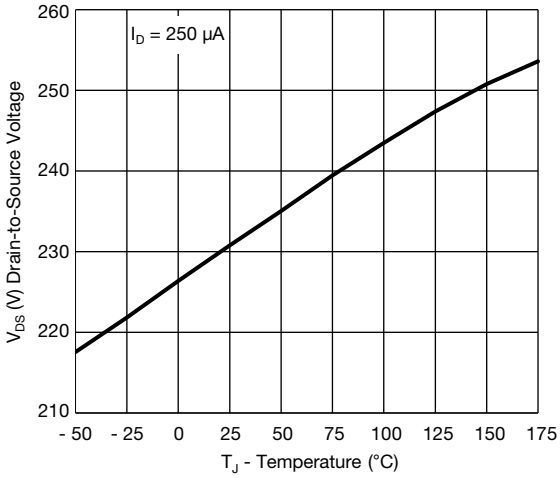

**On-Resistance vs. Junction Temperature**



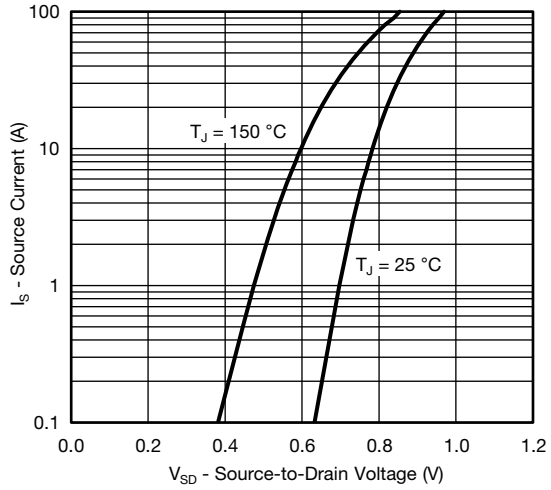

**Threshold Voltage**



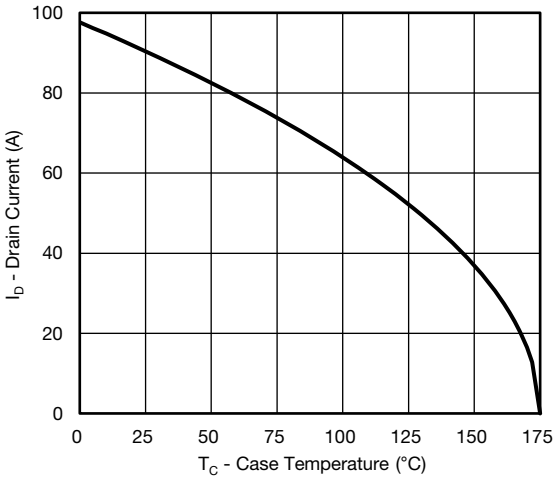

**On-Resistance vs. Gate-to-Source Voltage**




**Drain Source Breakdown vs. Junction Temperature**

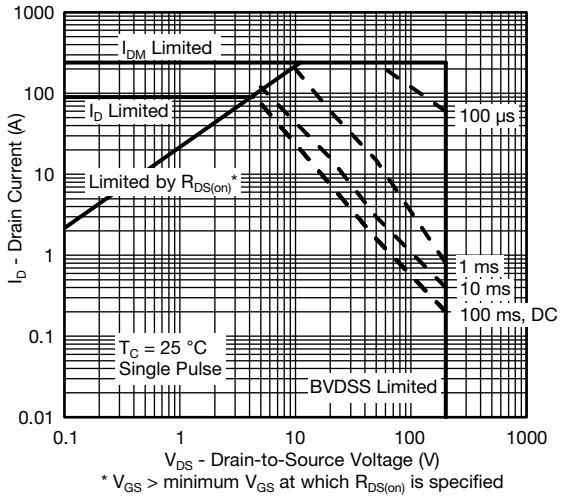



**Source Drain Diode Forward Voltage**

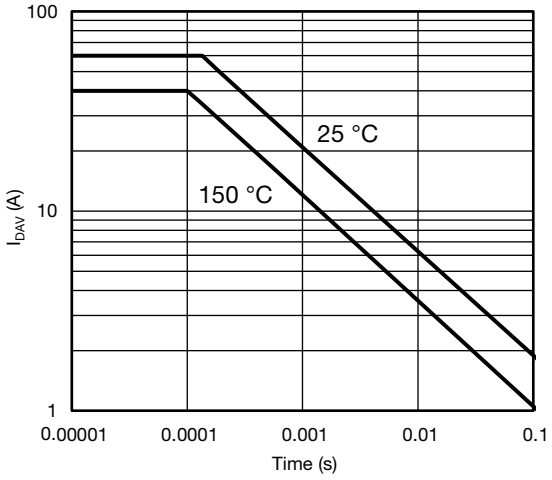



**Current De-rating**

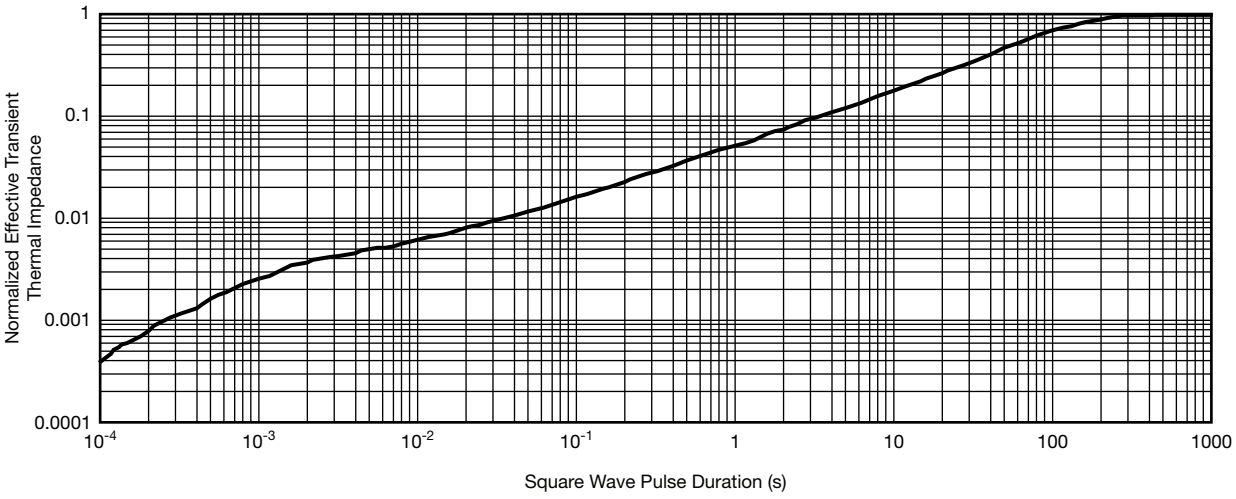
**THERMAL RATINGS** ($T_A$ = 25 °C, unless otherwise noted)




**Safe Operating Area**

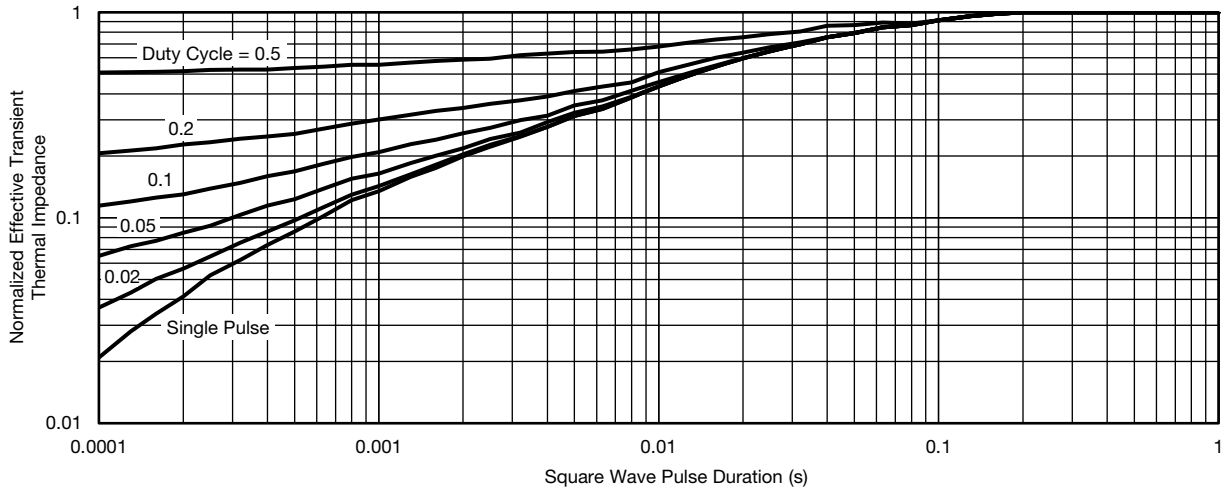



**Single Pulse Avalanche Current Capability vs. Time**




**Normalized Thermal Transient Impedance, Junction-to-Ambient**

**THERMAL RATINGS** ($T_A$ = 25 °C, unless otherwise noted)



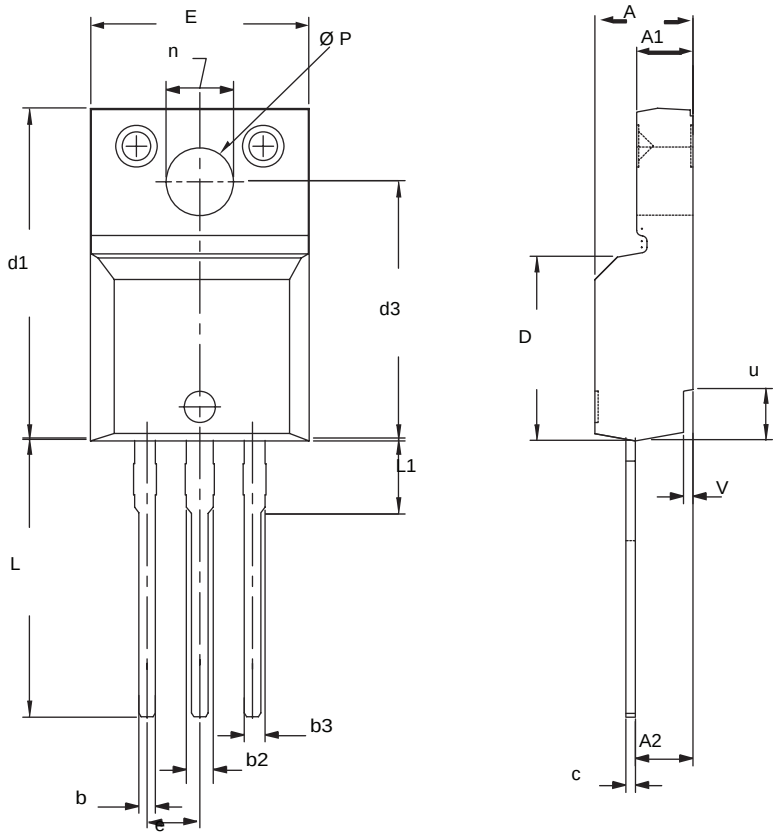

**Normalized Thermal Transient Impedance, Junction-to-Case**

**Note**

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction to Ambient (25 °C)
  - Normalized Transient Thermal Impedance Junction to Case (25 °C)

  are given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

## TO-220 FULLPAK (HIGH VOLTAGE)




| | MILLIMETERS | | INCHES | |
|---|---|---|---|---|
| DIM. | MIN. | MAX. | MIN. | MAX. |
| A | 4.570 | 4.830 | 0.180 | 0.190 |
| A1 | 2.570 | 2.830 | 0.101 | 0.111 |
| A2 | 2.510 | 2.850 | 0.099 | 0.112 |
| b | 0.622 | 0.890 | 0.024 | 0.035 |
| b2 | 1.229 | 1.400 | 0.048 | 0.055 |
| b3 | 1.229 | 1.400 | 0.048 | 0.055 |
| c | 0.440 | 0.629 | 0.017 | 0.025 |
| D | 8.650 | 9.800 | 0.341 | 0.386 |
| d1 | 15.88 | 16.120 | 0.622 | 0.635 |
| d3 | 12.300 | 12.920 | 0.484 | 0.509 |
| E | 10.360 | 10.630 | 0.408 | 0.419 |
| e | 2.54 BSC | | 0.100 BSC | |
| L | 13.200 | 13.730 | 0.520 | 0.541 |
| L1 | 3.100 | 3.500 | 0.122 | 0.138 |
| n | 6.050 | 6.150 | 0.238 | 0.242 |
| Ø P | 3.050 | 3.450 | 0.120 | 0.136 |
| u | 2.400 | 2.500 | 0.094 | 0.098 |
| v | 0.400 | 0.500 | 0.016 | 0.020 |

**Notes**

1. To be used only for process drawing.
2. These dimensions apply to all TO-220, FULLPAK leadframe versions 3 leads.
3. All critical dimensions should C meet $C_{pk} > 1.33$.
4. All dimensions include burrs and plating thickness.
5. No chipping or package damage.

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